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**FACSIMILE
TRANSMITTAL**

Date: February 2, 2005

To: Examiner Lindsay, Jr.
U.S. Patent and Trademark Office

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FEB 02 2005

Fax No: 703-872-9306

Ph. No:

From: Andrew J. Telesz, Jr.

Subject: Serial No.: 10/762,361
Our Ref. No.: OKI.206D3

No. of Pages (including cover): 9

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Comments:

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Via Facsimile 703-872-9306**OKI.206D3****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re PATENT APPLICATION of

**RECEIVED
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Masahiro Yoshida et al.

Group Art Unit: 2812

FEB 02 2005

Serial No.: 10/762,361

Examiner: W. Lindsay, Jr.

Filed: January 23, 2004

Conf. No.: 4915

**FOR: METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING
A MOSFET WITH NITRIDE SIDEWALLS****LETTER RESUBMITTING EARLIER FILED INFORMATION DISCLOSURE
STATEMENT**

U.S. Patent and Trademark Office
Customer Window, **Mail Stop Issue Fee**
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Date: February 2, 2005

Sir:

The Notice of Allowability dated January 21, 2005, does not acknowledge receipt of the Information Disclosure Statement filed on December 23, 2004.

Accordingly, enclosed is a copy of the Information Disclosure Statement and Form PTO/SB/08A form filed on December 23, 2004. Also enclosed is a copy of a dated, stamped postcard receipt provided as evidence that the above noted Information Disclosure Statement was received by the U.S. Patent Office on December 23, 2004.

**Applicants respectfully request the Examiner to acknowledge that the
Information Disclosure Statement has been received, and to confirm that the
documents will be cited of record.**

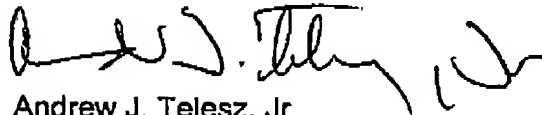
Serial No.: 10/762,361

In the event that there are any outstanding matters remaining in the present application, please contact Andrew J. Telesz, Jr. (Reg. No. 33,581) at (703) 715-0870 in the Washington, D.C. area, to discuss these matters.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment for any additional fees that may be required, or credit any overpayment, to Deposit Account No. 50-0238.

Respectfully submitted,

VOLENTINE FRANCOS & WHITT, P.L.L.C.



Andrew J. Telesz, Jr.
Registration No. 33,581

One Freedom Square
11951 Freedom Drive, Suite 1260
Reston, Virginia 20190
Telephone No.: (703) 715-0870
Facsimile No.: (703) 715-0877

Enclosures: • Copy of Information Disclosure Statement and PTO/SB/08A form filed on
December 23, 2004
• Copy of dated, stamped postcard receipt

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))Docket No.
OKL206D3

In Re Application Of: Masahiro Yoshida et al.

| Application No. | Filing Date | Examiner | Customer No. | Group Art Unit | Confirmation No. |
|-----------------|------------------|-----------------|--------------|----------------|------------------|
| 10/762,361 | January 23, 2004 | W. Lindsay, Jr. | | 2812 | 4915 |

Title: **METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING A MOSFET
WITH NITRIDE SIDEWALLS**Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☒ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☒ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).U.S. Patent No. 4,810,666 is provided as an English language equivalent of
JP 61-16571.

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))Docket No.
OKI.206D3

In Re Application: Masahiro Yoshida et al.

| Application No. | Filing Date | Examiner | Customer No. | Group Art Unit | Confirmation No. |
|-----------------|------------------|-----------------|--------------|----------------|------------------|
| 10/762,361 | January 23, 2004 | W. Lindsay, Jr. | | 2812 | 4915 |

Title: **METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING A MOSFET
WITH NITRIDE SIDEWALLS****Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0238 as described below.
- ☐ Charge the amount of _____
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***This certificate may only be used if paying by deposit account.**

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ANDREW J. TELESZ, JR.
REG. NO. 33,581

Dated: December 23, 2004

VOLENTINE FRANCOS & WHITT, P.L.L.C.
ONE FREEDOM SQUARE
11951 FREEDOM DRIVE, SUITE 1260
RESTON, VA 20190
TEL. NO. (703) 715-0870

cc:

**STATEMENT UNDER 37 CFR 1.97(e) ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**Docket No.
OKI.206D3In Re Application Of: **Masahiro Yoshida et al.**

| Application No. | Filing Date | Examiner | Customer No. | Group Art Unit | Confirmation No. |
|-----------------|------------------|-----------------|--------------|----------------|------------------|
| 10/762,361 | January 23, 2004 | W. Lindsay, Jr. | | 2812 | 4915 |

Invention: **METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING A
MOSFET WITH NITRIDE SIDEWALLS****COMMISSIONER FOR PATENTS:**

This is a statement under the provisions of 37 CFR 1.97(e) in the above-identified application.

Check applicable statement herebelow:

Statement Under 37 CFR 1.97(e)(1)

- ☒ Each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

Statement Under 37 CFR 1.97(e)(2)

- ☐ No item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned person, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.


*Signature***ANDREW J. TELESZ, JR.**
REG. NO. 33,581**VOLENTINE FRANCOS & WHITT, P.L.L.C.**
ONE FREEDOM SQUARE
11951 FREEDOM DRIVE, SUITE 1260
RESTON, VA 20190
TEL. NO. (703) 715-0870Dated: **December 23, 2004****Certificate of Mailing by First Class Mail**

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PTO/SB/DBA (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

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| Application Number | 10/762,361 |
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| Filing Date | January 23, 2004 |
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| First Named Inventor | Masahiro Yoshida et |
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| Art Unit | 2812 |
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| Examiner Name | W. Lindsay, Jr. |
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| Attorney Docket Number | OK-206D3 |
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[illegible]

| FOREIGN PATENT DOCUMENTS | | | | | | |
|--------------------------|----------|---|------------------|---|---|----|
| Examiner Initials* | Cite No. | Foreign Patent Document | Publication Date | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear | Y* |
| | | Country Code* Number* Kind Code* (if known) | MM-DD-YYYY | | | |
| | | JP 09-045908 | 02/14/1997 | | | ✓ |
| | | JP 05-198794 | 08/06/1993 | | | ✓ |
| | | JP 61-16571 | 01/24/1986 | | | |
| | | JP 09-008307 | 01/10/1997 | | | ✓ |
| | | JP 09-129872 | 05/16/1997 | | | ✓ |
| | | | | | | |

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

ATTY DOCKET #: OKI.206D3**APPLICANT:** Masahiro Yoshida et al.**DUE DATE:****SERIAL NO.:** 10/762,361**FILING DATE:** January 23, 2004**TITLE:** METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE
INCLUDING A MOSFET WITH NITRIDE SIDEWALLS**RECEIPT OF THE FOLLOWING PAPERS IS ACKNOWLEDGED:**

Transmittal of Information Disclosure Statement; Statement Under 37 C.F.R.
1.97(e); PTO/SB/08A Form; English translation of Japanese Office Action; and
Six (6) references.

**DATE:** December 23, 2004 **ATTY:** AJT**[Check No. N/A]**